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Sanchis, A.; Jimenez, V.; Vidal, E.;
Pattern Recognition, 2000. Proceedings. 15th International Conference on , Volume: 3 , 3-7 Sept. 2000
Pages:274 - 277 vol.3

[Abstract] [PDF Full-Text (348 KB)] **IEEE CNF**

2 **Learning patterns of activity using real-time tracking**
Stauffer, C.; Grimson, W.E.L.;
Pattern Analysis and Machine Intelligence, IEEE Transactions on , Volume: 22 , Issue: 8 , Aug. 2000
Pages:747 - 757

[Abstract] [PDF Full-Text (2296 KB)] **IEEE JNL**

3 **The application of cepstral coefficients and maximum likelihood method in EMG pattern recognition [movements classification]**
Wen-Juh Kang; Jiue-Rou Shiu; Cheng-Kung Cheng; Jin-Shin Lai; Hen-Wai Tsao; Te-Son Kuo;
Biomedical Engineering, IEEE Transactions on , Volume: 42 , Issu

8 , Aug. 1995
Pages:777 - 785

[Abstract] [PDF Full-Text (756 KB)] **IEEE JNL**

4 **Models for generating agents in electricity markets**
Roman, J.;
Power Engineering Society Winter Meeting, 2000. IEEE , Volume: 3 , 23-27 Jan. 2000
Pages:1697 vol.3

[Abstract] [PDF Full-Text (48 KB)] **IEEE CNF**

5 **Gesture patterns during speech repairs**
Chen, L.; Harper, M.; Quek, F.;
Multimodal Interfaces, 2002. Proceedings. Fourth IEEE Internatic Conference on , 14-16 Oct. 2002
Pages:155 - 160

[Abstract] [PDF Full-Text (385 KB)] **IEEE CNF**

6 **Use of characteristic views in image classification**
Chih-Cheng Hung; Shisong Yang; Laymon, C.;
Pattern Recognition, 2002. Proceedings. 16th International Conference on , Volume: 2 , 11-15 Aug. 2002
Pages:949 - 952 vol.2

[Abstract] [PDF Full-Text (365 KB)] **IEEE CNF**

7 **Removal of redundancy in logic circuits under classificat of undetectable faults**
Kajihara, S.; Shiba, H.; Kinoshita, K.;
Fault-Tolerant Computing, 1992. FTCS-22. Digest of Papers., Twenty-Second International Symposium on , 8-10 July 1992
Pages:263 - 270

[Abstract] [PDF Full-Text (500 KB)] **IEEE CNF**

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1			JP 10293586 A	19981104	14	AUTOMATIC ACCOMPANIMENT DEVICE
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3			JP 06348271 A	19941222	18	AUTOMATIC ACCOMPANIMENT DEVICE
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	Current OR	Current XRef	Retrieval Classif	Inventor	S	C	P	2	3
1				KONDO, MASAO et al.	X				
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4				KONDO, MASAO et al.	X				

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1			JP 10293586 A	
2			JP 08185180 A	
3			JP 06348271 A	
4			JP 06337677 A	

	U	1	Document ID	Issue Date	Pages	Title
1			US 5253182 A	19931012	21	Method of and apparatus for converting design pattern data to exposure data

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1	716/21	250/492.1 ; 250/492.3		Suzuki; Toshio	X				

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1			US 5253182	

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1			US 5790692 A	19980804	33	Method and means of least squares designed filters for image segmentation in scanning cytometry

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1	382/133	382/173; 382/205; 382/265		Price; Jeffrey H. et al.	X				

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1			US 5790692	

	U	1	Document ID	Issue Date	Pages	Title
1			US 5790692 A	19980804	33	Method and means of least squares designed filters for image segmentation in scanning cytometry
2			US 5704019 A	19971230	40	Resolution conversion method and apparatus
3			US 4916512 A	19900410	82	Photoelectric converter

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1	382/133	382/173; 382/205; 382/265		Price; Jeffrey H. et al.	X				
2	358/1.1	358/447; 358/451; 382/258; 382/298; 382/299		Akiyama; Yuji et al.	X				
3	348/314	257/290; 257/443; 257/462; 257/E27.149; 257/E31.069		Ohmi; Tadahiro et al.	X				

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Last Name = YOSHIZAWA
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Application#	Patent#	Status	Date Filed	Title	Inventor Name 7
10134842	Not Issued	030	04/29/2002	SIZING PROCESSING SYSTEM AND COMPUTER PROGRAM FOR THE SAME	YOSHIZAWA, KEIJI
10067061	Not Issued	030	02/04/2002	METHOD OF EFFICIENTLY CONVERTING-TO-ARRAY AND COMPRESSING DATA IN A PROCESS FOR CONVERTING MASK PATTERNS OF A LSI	YOSHIZAWA, KEIJI
09979515	6492616	150	01/14/2002	PROCESSES FOR LASER BEAM MACHINING OF RESIN FILM FOR WIRING BOARDS AND MANUFACTURE OF WIRING BOARDS	YOSHIZAWA, KEIJI
09886354	Not Issued	041	06/21/2001	REDUCTION PROCESSING METHOD AND COMPUTER READABLE STORAGE MEDIUM HAVING PROGRAM STORED THEREON FOR CAUSING COMPUTER TO EXECUTE THE METHOD	YOSHIZAWA, KEIJI
09492924	6577991	150	01/28/2000	RECORDABLE METHOD OF PROCESSING FIGURE	YOSHIZAWA, KEIJI
08395516	5601905	150	02/27/1995	LAMINATE FOR INSULATION PROTECTION OF CIRCUIT BOARDS	YOSHIZAWA , KEIJI
08027843	Not Issued	166	03/08/1993	LAMINATE, PROCESS FOR USE THEREOF AND PROCESS FOR PREPARATION OF PRINTED CIRCUIT	YOSHIZAWA , KEIJI

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